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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/864,561	05/23/2001	Hee Wong	50019.54US01/P04766	2859

23552 7590 12/04/2002

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EXAMINER

LANEAU, RONALD

ART UNIT	PAPER NUMBER
2674	

DATE MAILED: 12/04/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No.	Applicant(s)	
	09/864,561	WONG, HEE	
	Examiner Ronald Laneau	Art Unit 2674	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) Responsive to communication(s) filed on 23 May 2001.
- 2a) This action is **FINAL**.                                    2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) Claim(s) 13-20 is/are allowed.
- 6) Claim(s) 1-7 and 9-11 is/are rejected.
- 7) Claim(s) 8 and 12 is/are objected to.
- 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 29 October 2002 is/are: a) accepted or b) objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) The proposed drawing correction filed on \_\_\_\_\_ is: a) approved b) disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.
- 12) The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) All b) Some \* c) None of:
  1. Certified copies of the priority documents have been received.
  2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

- 14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).  
a) The translation of the foreign language provisional application has been received.
- 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s) _____.
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) <u>3</u> .	6) <input type="checkbox"/> Other: _____.

## **DETAILED ACTION**

1. Claims 1-20 are presented for Examination. The results of the examination are the followings.

### ***Drawings***

2. The formal drawings submitted on 10/29/02 (paper # 2) are placed in the application file.

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in-

- (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effect under this subsection of a national application published under section 122(b) only if the international application designating the United States was published under Article 21(2)(a) of such treaty in the English language; or
- (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that a patent shall not be deemed filed in the United States for the purposes of this subsection based on the filing of an international application filed under the treaty defined in section 351(a).

4. Claims 1-7 and 9-11 are rejected under 35 U.S.C. 102(e) as being anticipated by Ishii et al (US 2002/0036723).

As per claim 1, Ishii et al teach receiving a video data signal having inherently a predetermined pixel frequency, the video data signal being provided by the video data signal circuitry, providing a local clock signal clk1 to re-clock the video data between the video data signal circuitry and output circuit 40, thereby removing interfering influence of other clock signals on the predetermined pixel frequency as claimed (see fig. 6).

As per claims 2 and 3, Ishii et al teach a video data signal which is received and generated within an integrated video display system as claimed since the circuit 30 is part of the whole display system (see fig. 6).

As per claim 4, Ishii et al teach an output circuitry that comprises a digital-to-analog (D/A) converter subcircuit 33 as claimed (see fig. 6).

As per claims 5 and 6, Ishii et al teach a clock generation circuit 34 (latching or flip-flop) configured to latch the video data to the output circuit 40 wherein the flip-flop is being clocked by a local clock signal clk2, clk3, clk4 and being coupled to the output circuit. (see fig. 6).

As per claim 7, Ishii et al teach a video data circuit to an output circuit 40 through a latching circuit 34 as the clock clk2, clk3, clk4 can change states

(highs/lows) like a flip-flop, the video data circuit being inherently configured to provide a video data signal based on a pixel frequency, and a re-clocking circuit coupled to the latching circuit 34, the re-clocking circuit being configured to provide a local clock signal clk1 for re-clocking the video data signal through the latching circuit 34, wherein the video data signal is provided to the output circuit based on the local clock signal (see fig. 6).

As per claims 9 and 10, Ishii et al teach a clock generation circuit 34 (latching or flip-flop) configured to latch the video data to the output circuit 40 wherein the flip-flop is being clocked by a local clock signal clk2, clk3, clk4 and being coupled to the output circuit. (see fig. 6).

As per claim 11, Ishii et al teach an output circuitry that comprises a digital-to-analog (D/A) converter subcircuit 33 as claimed (see fig. 6).

***Allowable Subject Matter***

5. Claims 8 and 12 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

As per claim 8, an integrated video wherein the pixel frequency is based on an external clock reference and wherein the re-clocking circuit is based on the external clock reference.

As per claim 12, an integrated video display system further comprising a selection circuit for selectively switching between conventionally clocking the video data signal based on the pixel frequency and re-clocking the video data signal based on the local clock.

Claims 13-20 are allowed.

None of the references, either singularly or in combination, teaches or even suggests:

As per claims 13-20, an integrated video display system for providing a video signal having reduced beat patterns comprising:

a re-clocking circuit having a frequency based on a clock signal provided by a local clock generator; and

a select switch for selectively coupling the video data source to the output circuit based on either the conventional clocking circuit or the re-clocking circuit,

wherein interfering influence of other clock signals on the predetermined pixel frequency is removed if the re-clocking circuit is coupled between the video data source and the output circuit.

8. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Rindal et al (US 6,020,939) teach a method and apparatus for reducing electromagnetic interference radiated by Cathode Ray Tube Displays.
- Hijikata (US 5,475,402) teaches a display control apparatus comprising a control signal generator for generating various control signals including pixel video data and a display clock signal synchronous to the pixel video data.

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ronald Laneau whose telephone number is 703-305-3973. The examiner can normally be reached on Monday-Thursday from 8:00 AM to 6.00 PM or via email: ronald.laneau@uspto.gov.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached at 703-305-4709.

**10. Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

**or faxed to:**

**(703) 872-9314 (for Technology Center 2600 only)**

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

*Ronald Laneau*  
Ronald Laneau  
Examiner  
Art Unit 2674

rl  
November 25, 2002